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... Statistical Dependences Detailed **trace**, with statistical ... Simulation **Instructions**,

Cache, **Branch** prediction events and **instruction** dependences all ...

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... use a large buffer and decrease the number of calls to the **trace** reader. ... in the total number of **branches** taken (about 1.4 **branches** per iteration ... **Instructions/sec** ...

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... *129.**compress**. **130.li. ... **Instruction** and data **Event Address Registers (EAR)**. **Branch** **trace** buffer. ... Clock cycle counter (ITC register). Retired **instruction** counter. ...

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... Dependencies Statistical Cache+**Branch**+Dependencies Statistical ... of approximation of the **trace**, simulated on base architecture (100 million **instructions**) Figure 3 ...

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... the branchmisprediction rate and the sustained **instructions**-per-cycle. ... Table 7 **Trace** sampling benchmarks. Name Description Length (# instr) IPC **Branch** Mispred. ...

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... ...**trace** generators ... a waste of time, since the **condition** codes are used in **branch** decisions much ... Shade: a fast instruction-set simulator for execution pro ling ...

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... Program **Trace** Messaging is a means for observing ... Subsequent changes of program **instruction** flow messages will ... Direct **branch** messages are shorter since only the ...

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